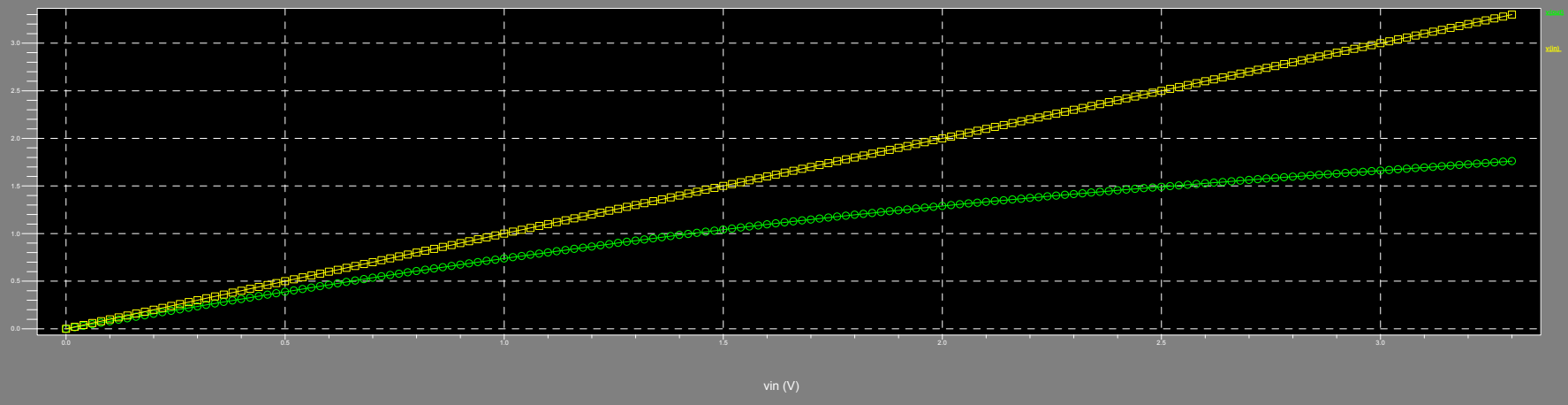


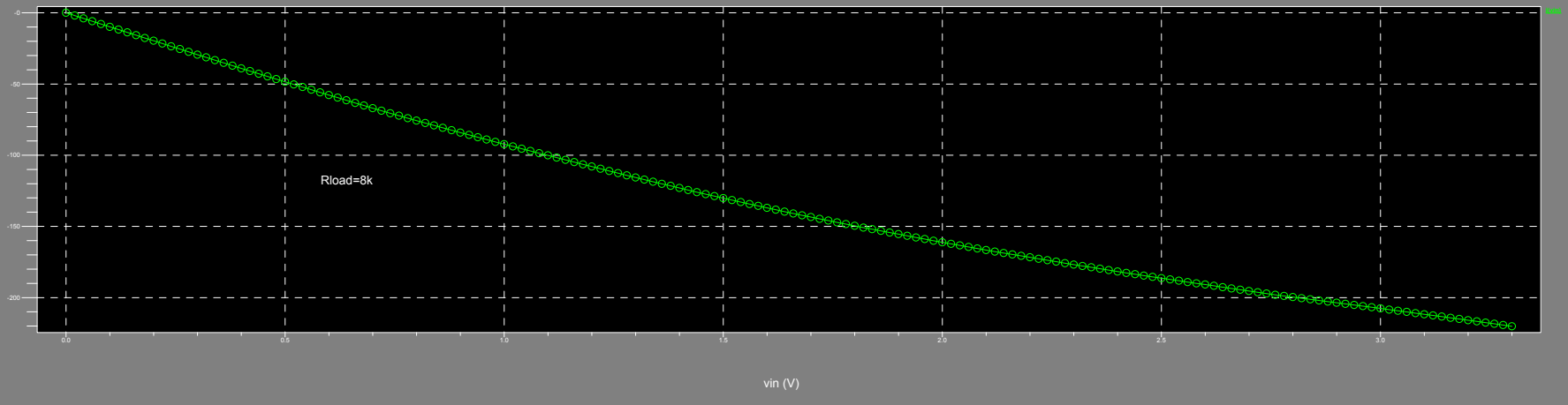
Voltage (V)

invert2



Current ( $\mu$ A)

invert2



(C-B)/D (k), Current (kA)

Gate Channel Resistance

