

A total solution including integrated design flows with cell libraries and support, deep-submicron netlist-to-layout service, design support, flexible access to silicon prototyping and production at leading foundries, packaging and test

Europractice Seminar Agenda :

08:30		Registration + coffee	
09:30	09:45	Welcome	IMEC
09:45	10:30	The Europractice IC Service	IMEC + FhG
10:30	12:00	Foundry and Library Partner presentations	austriamicrosystems, Austria AMI Semiconductor, Belgium UMC, Taiwan Virtual Silicon, USA
12:00	14:00	Lunch	
14:00	14:40	Test and Packaging Partner presentations	ASE, Taiwan Microtest, Italy
14:40	16:00	ASIC Design and IP Provider presentations	Chipldea, Portugal Dolphin Integration, France C4I, France Cambridge Consultants, UK
16:00	16:30	Coffee	
16:30	17:10	University Customer presentations	SUPAERO, France KULeuven, Belgium
17:10	17:20	Conclusions	IMEC
17:20	18:30	Reception and discussions	

Date & Location

December 10, 2003

IMEC
Kapeldreef 75
B-3001 Leuven, Belgium
Tel : +32-(0)16-281571
Seminar begins at 9:30am
Registration begins at 8:30am

Organized in addition to the Europractice Seminar :
AMIS 0.35 μ CMOS and 0.35 μ I3T80 Design Kit Training — 11&12 December 2003 — IMEC, Belgium

THE RIGHT COCKTAIL OF ASIC SERVICES