



EUROPRACTICE
I C S E R V I C E

AMIS 0.35 μ CMOS AND 0.35 μ I3T80 DESIGN KIT TRAINING 11 & 12 DECEMBER 2003

IMEC is organizing together with AMI Semiconductor and Lunds University a practical training on AMIS 0.35 μ CMOS and I3T80 design kits



Training Agenda :

11 December 2003

Morning	AMIS key technologies overview 0.7 μ CMOS I2T100 high voltage 0.35 μ CMOS logic and mixed 0.35 μ CMOS I3T80 high voltage
Afternoon	Full Custom design in 0.35μ CMOS using Cadence Schematic Entry Simulation with Spectre Layout DRC and LVS checking with Calibre Extra AMIS design kit options such as matching, Safe Operating Area checking, corner simulation, ...

12 December 2003

Morning	Digital Front-End Design with AMIS ADS design kit Design For Test Digital simulation and synthesis, wire load models Timing Analysis
Afternoon	Import to SE Floorplanning Pad Placement Block and Cell -Placement Clock Tree Generation Power -and Signal Routing Design Verification

Date & Location of the AMIS training

December 11 + 12, 2003

IMEC
Kapeldreef 75
B-3001 Leuven, Belgium
Tel : +32-(0)16-281571

Fee : 200 € (max. 20 participants)

You are also invited to the Europractice Seminar

December 10, 2003

IMEC
Kapeldreef 75
B-3001 Leuven, Belgium
Tel : +32-(0)16-281571
Fee : free entrance

THE RIGHT COCKTAIL OF ASIC SERVICES

Please register via E-mail : mpc@imec.be

www.europractice.imec.be