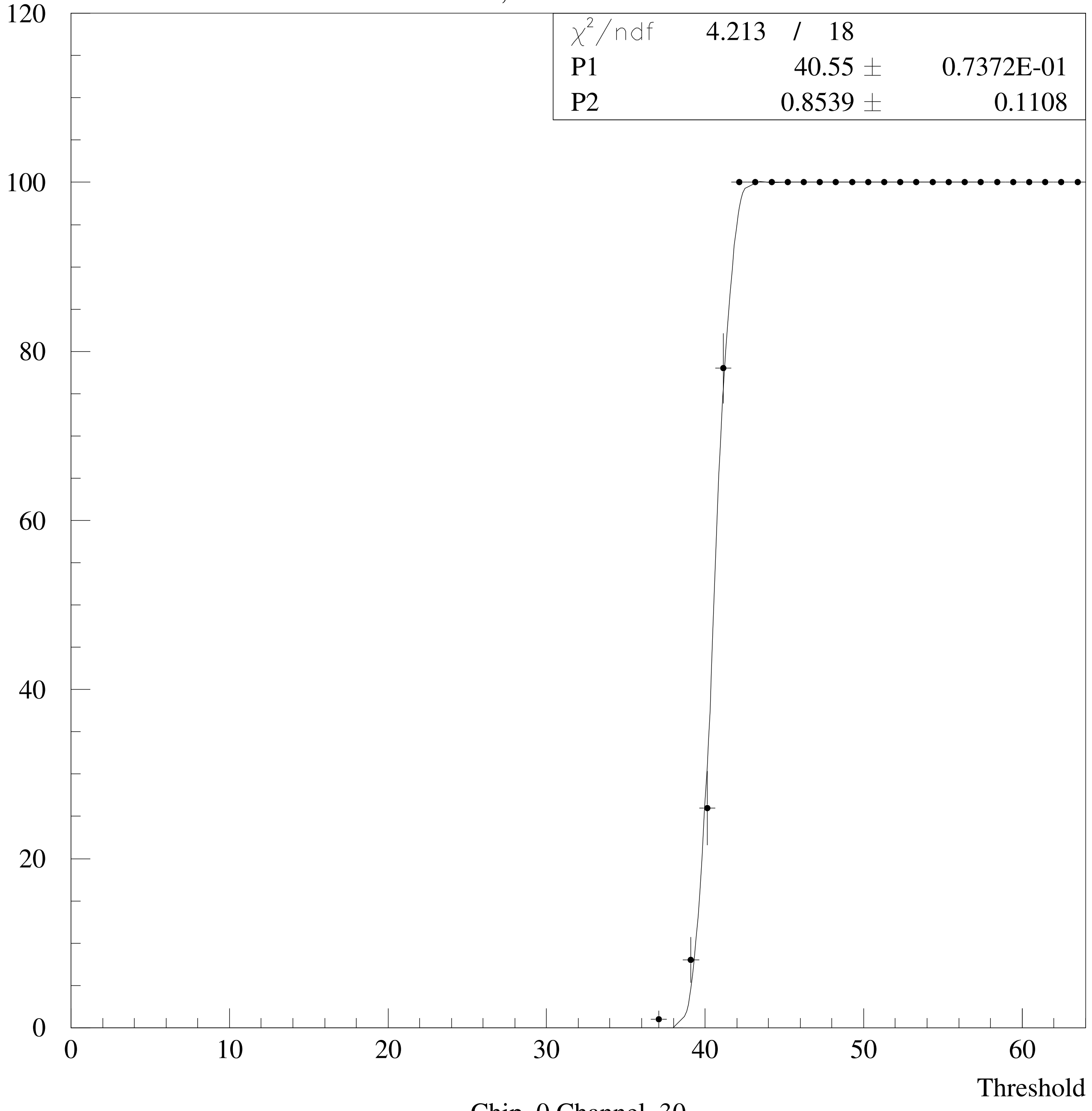
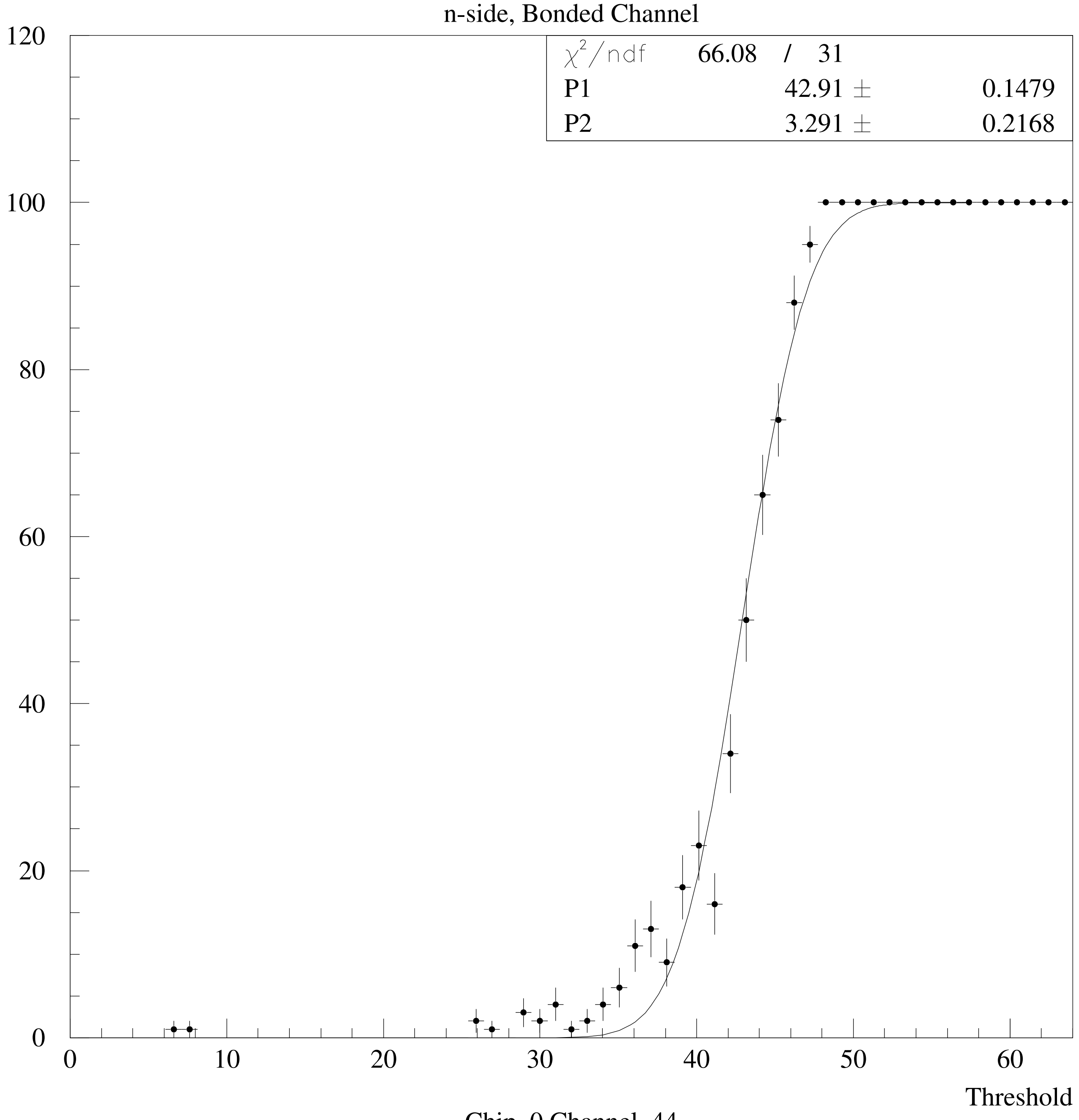
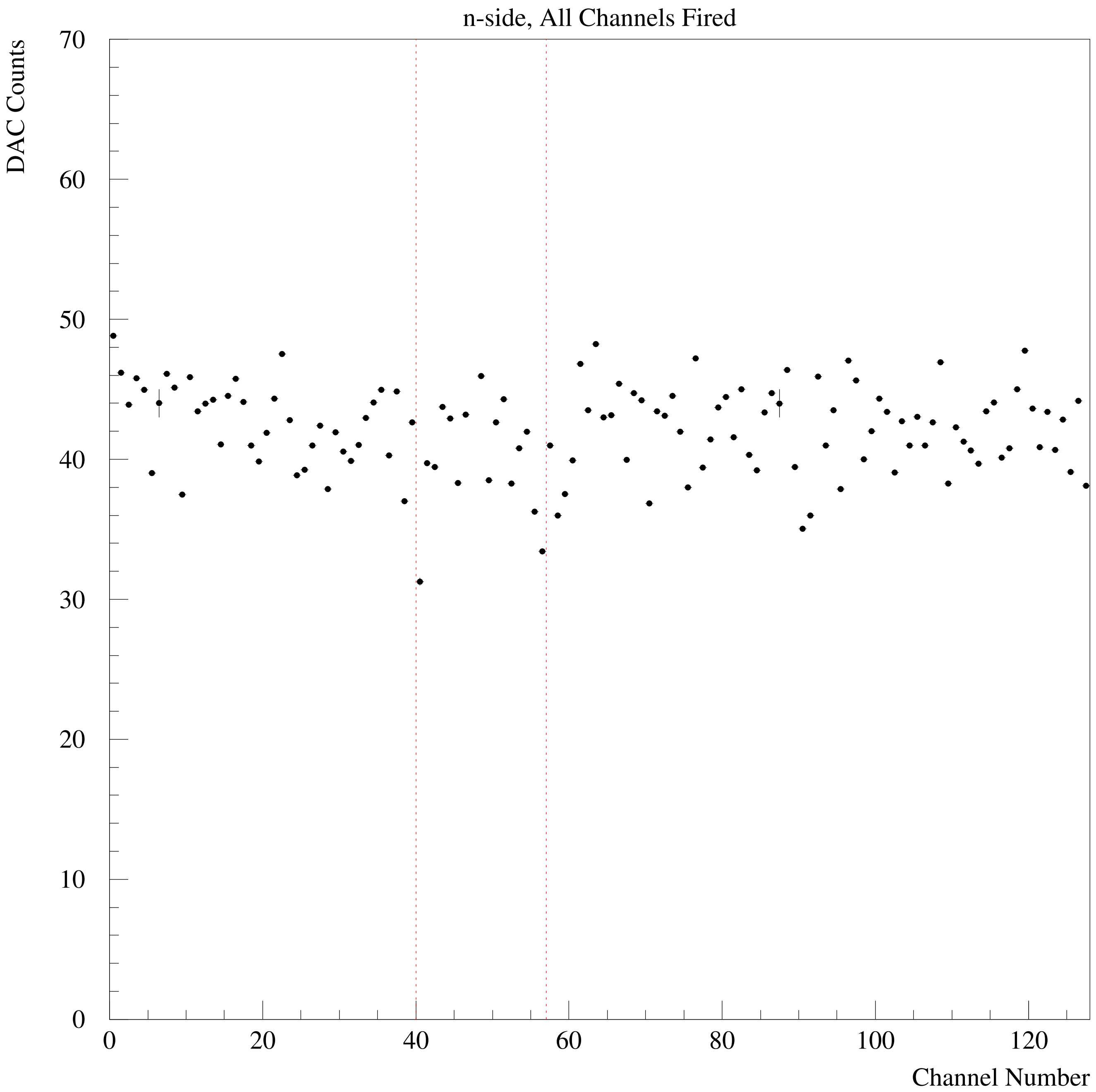
n-side, Unbonded Channel



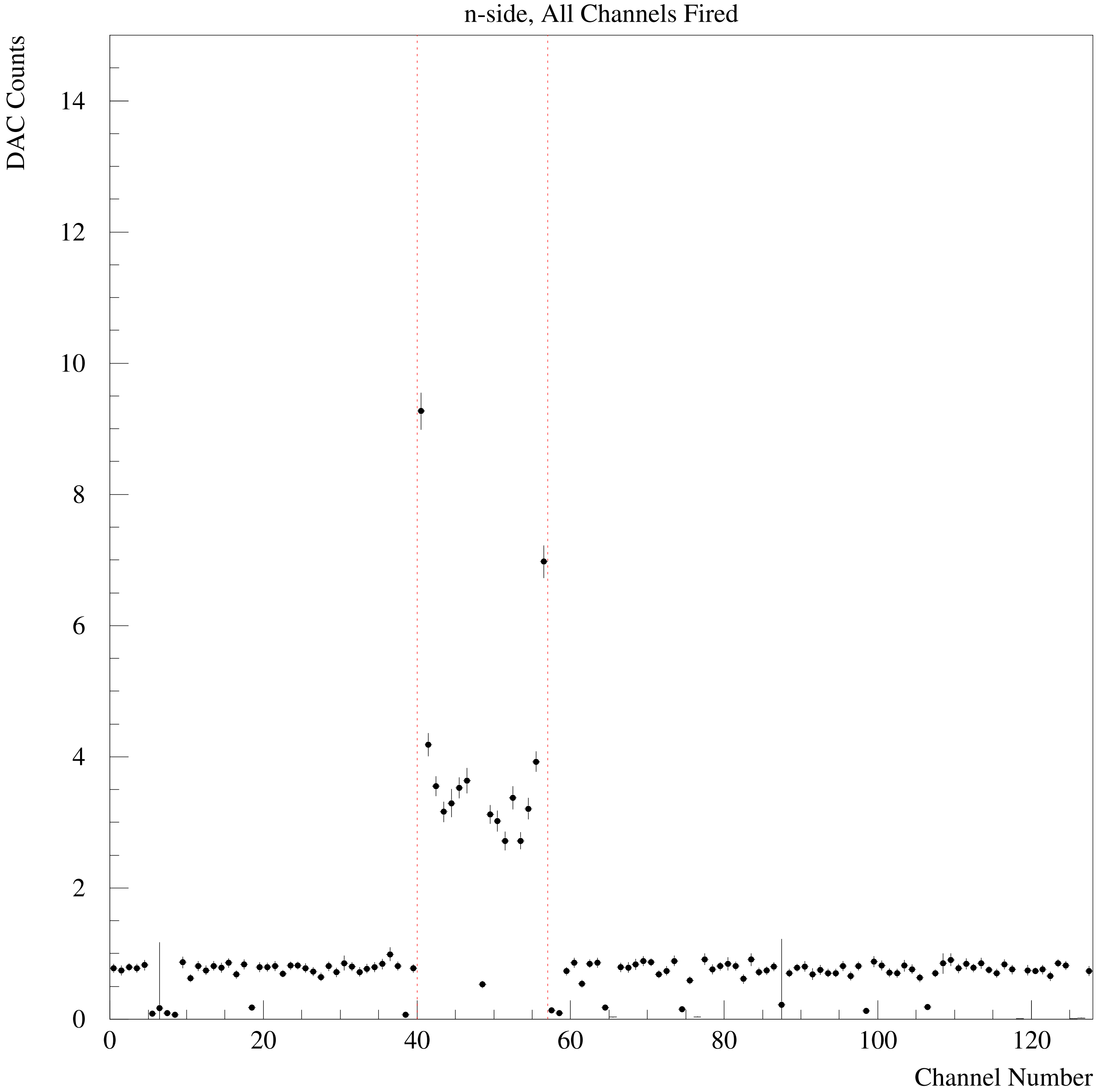
Chip 0 Channel 30



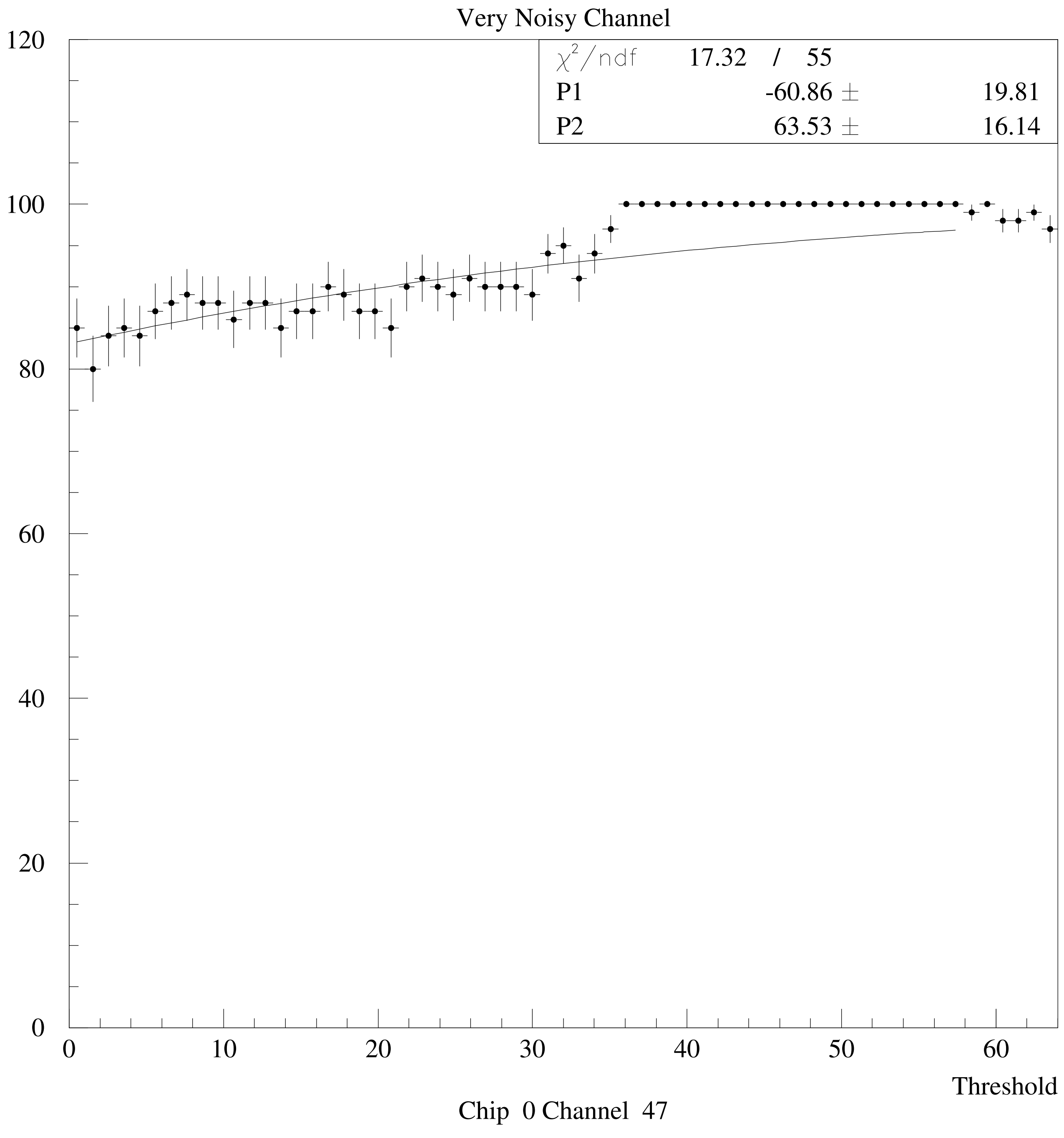
Chip 0 Channel 44

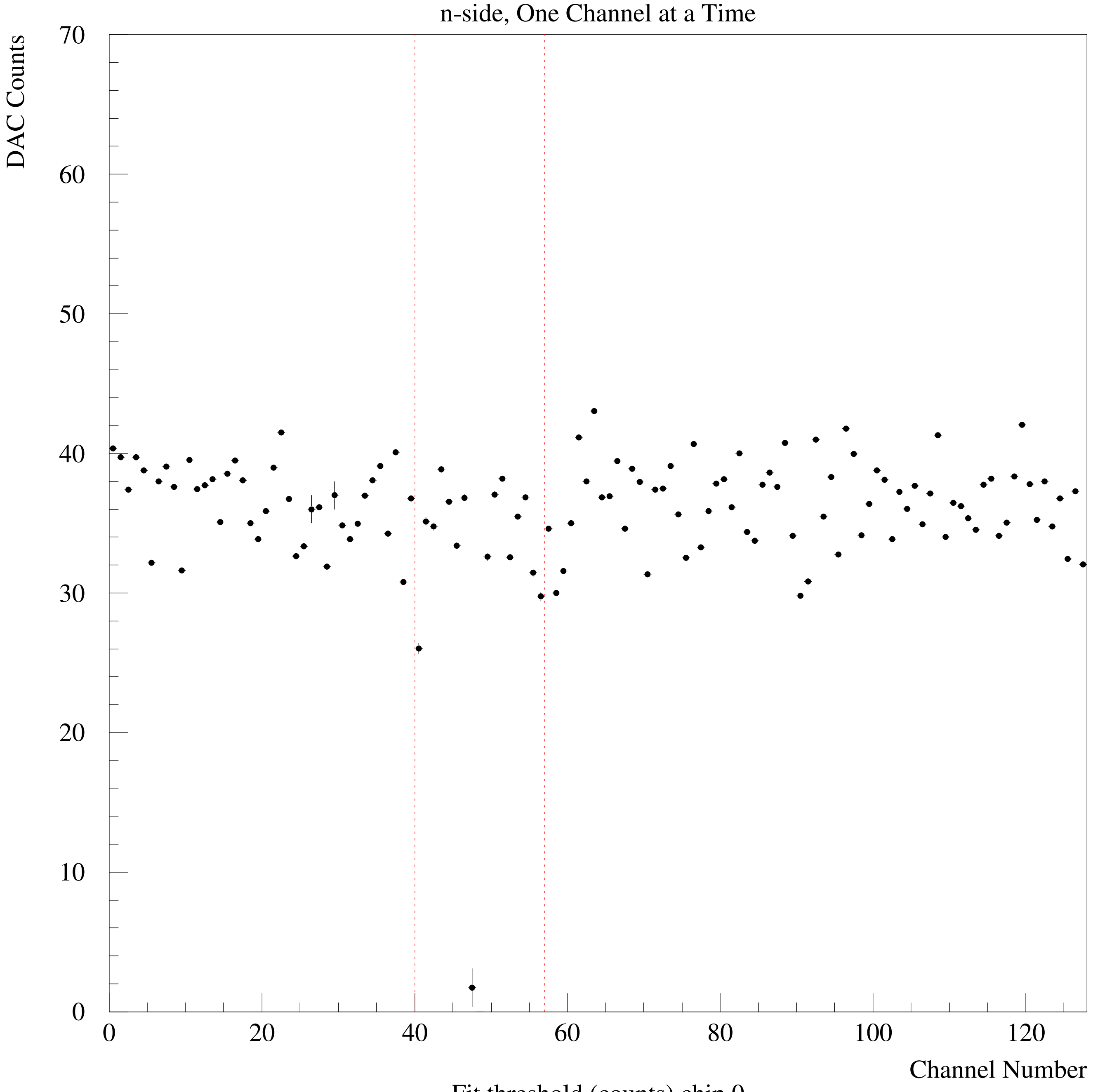


Fit threshold (counts) chip 0

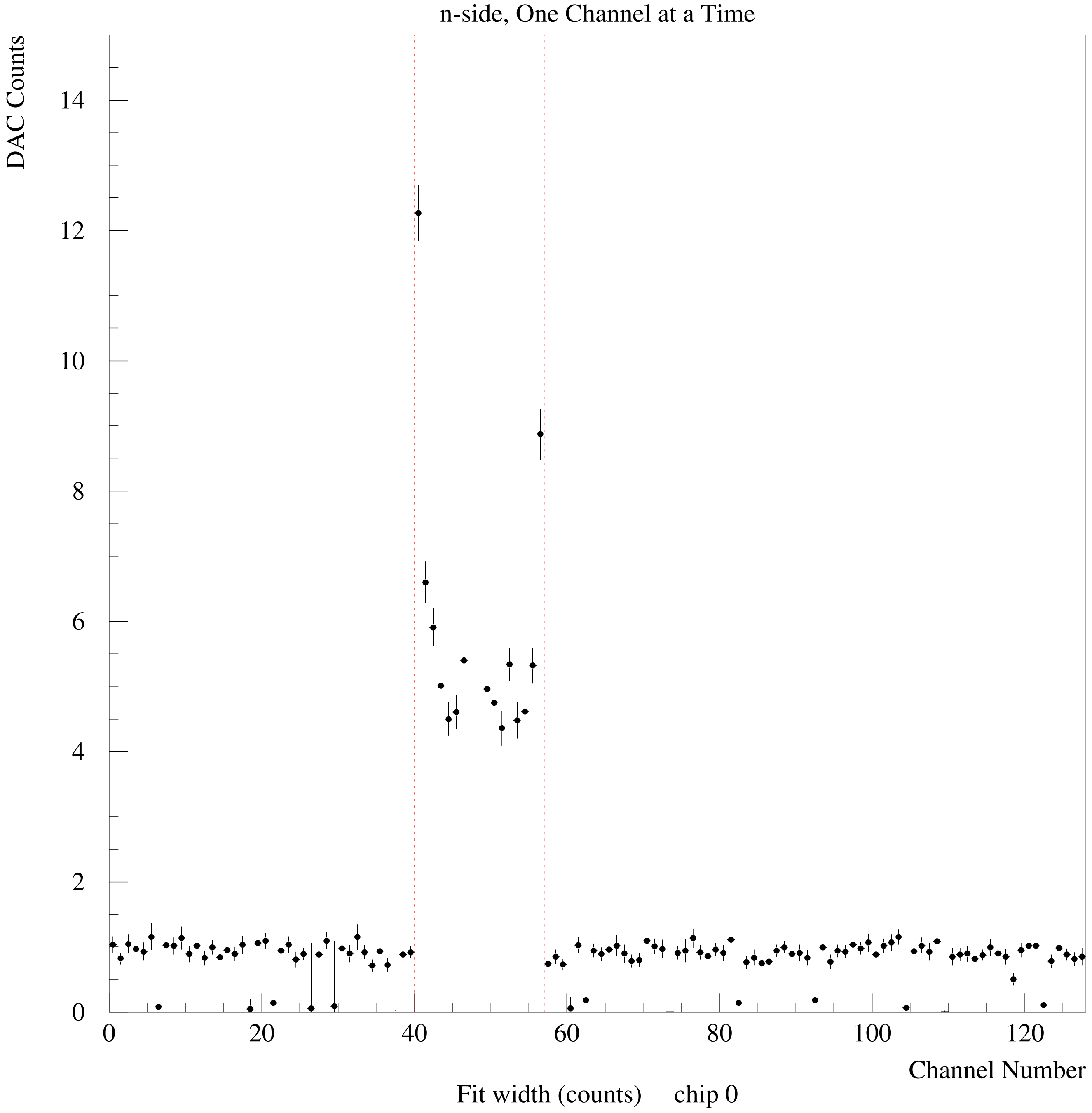


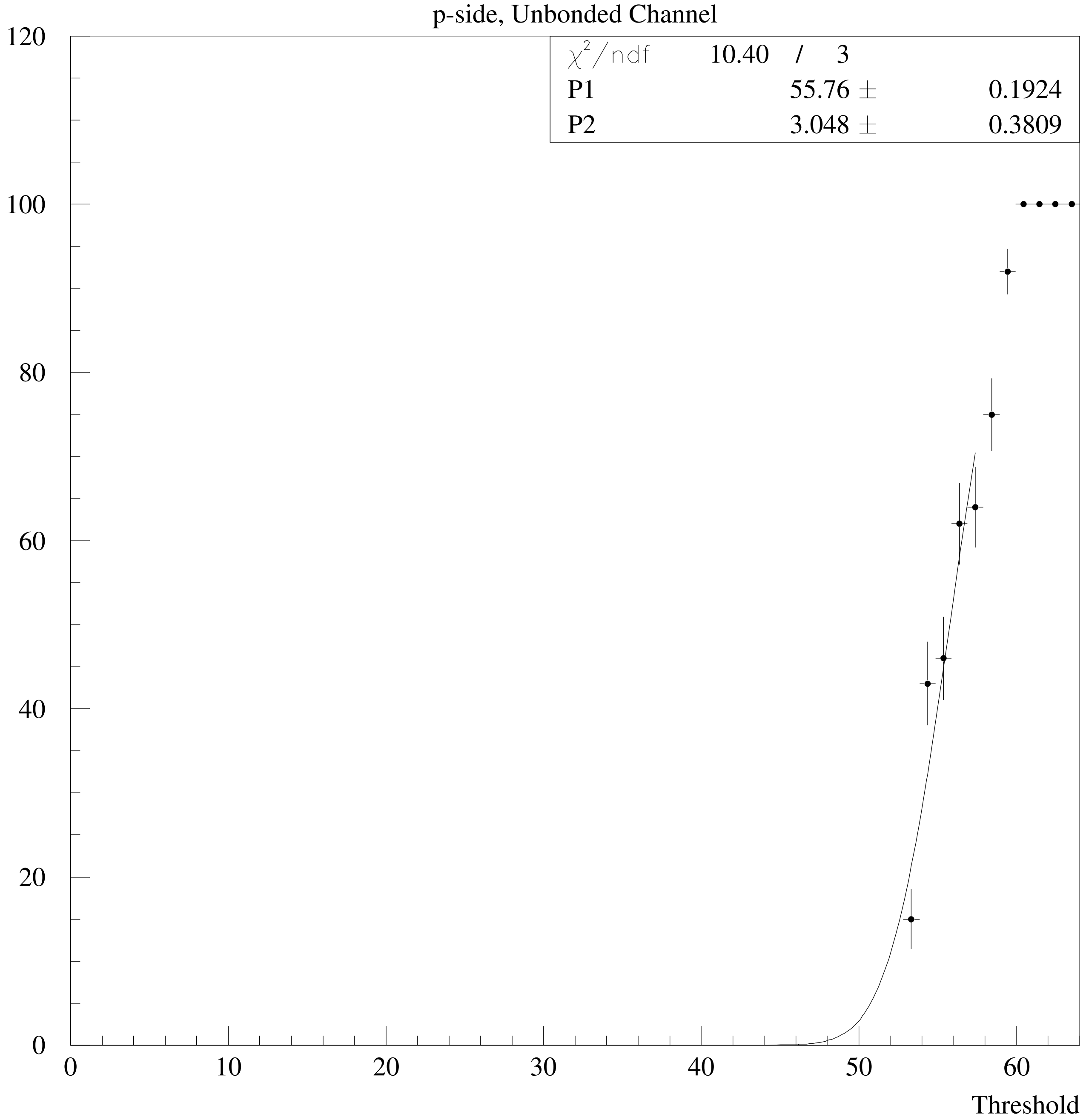
Fit width (counts) chip 0


Very Noisy Channel
χ^2/ndf 17.32 / 55
P1 -60.86 ± 19.81
P2 63.53 ± 16.14
120
100
80
60
40
20
0
0
10
20
30
40
50
60
Threshold
Chip 0 Channel 47

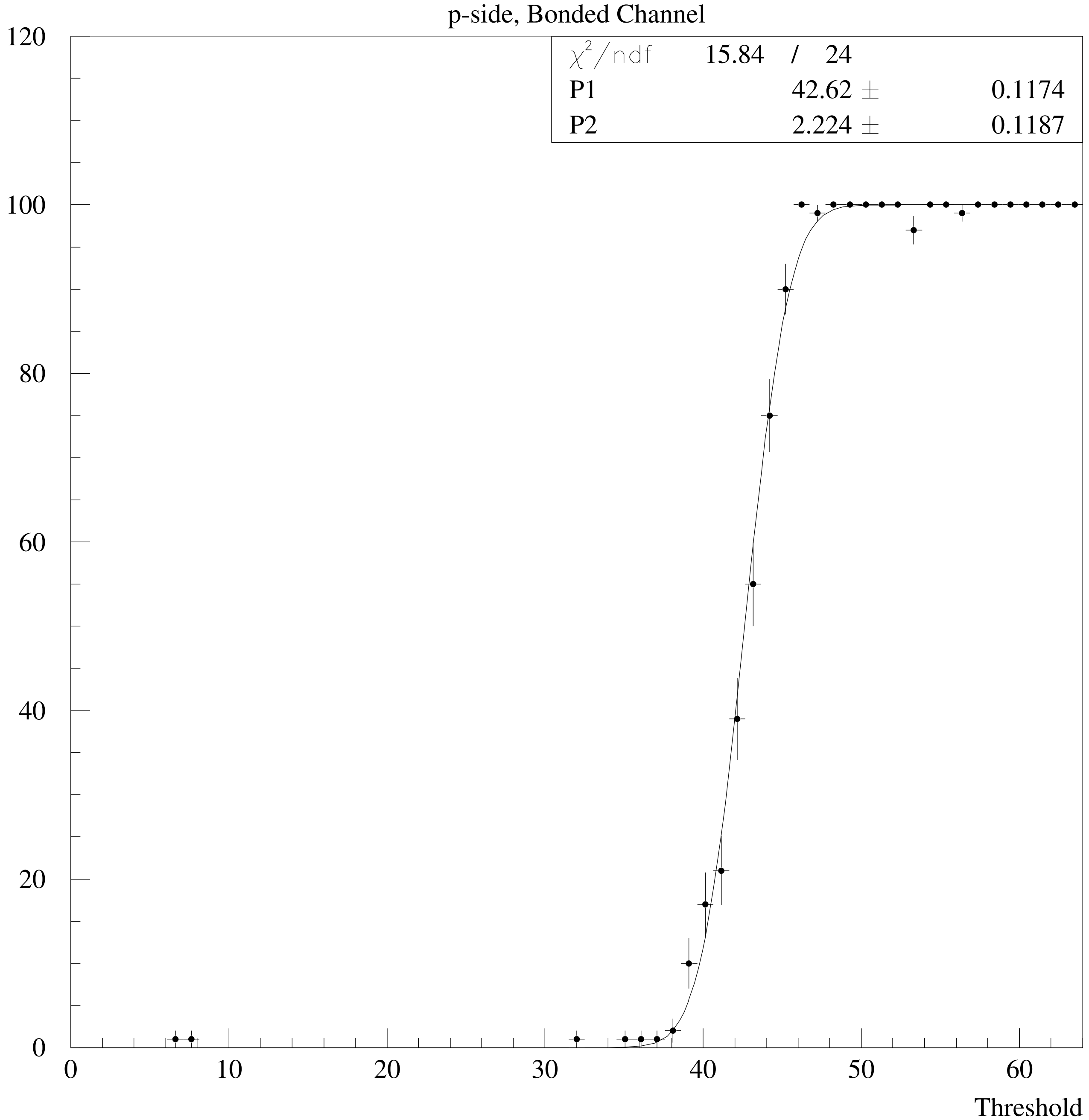


Fit threshold (counts) chip 0


n-side, One Channel at a Time
DAC Counts
14
12
10
8
6
4
2
0
0
20
40
60
80
100
120
Channel Number
Fit width (counts) chip 0

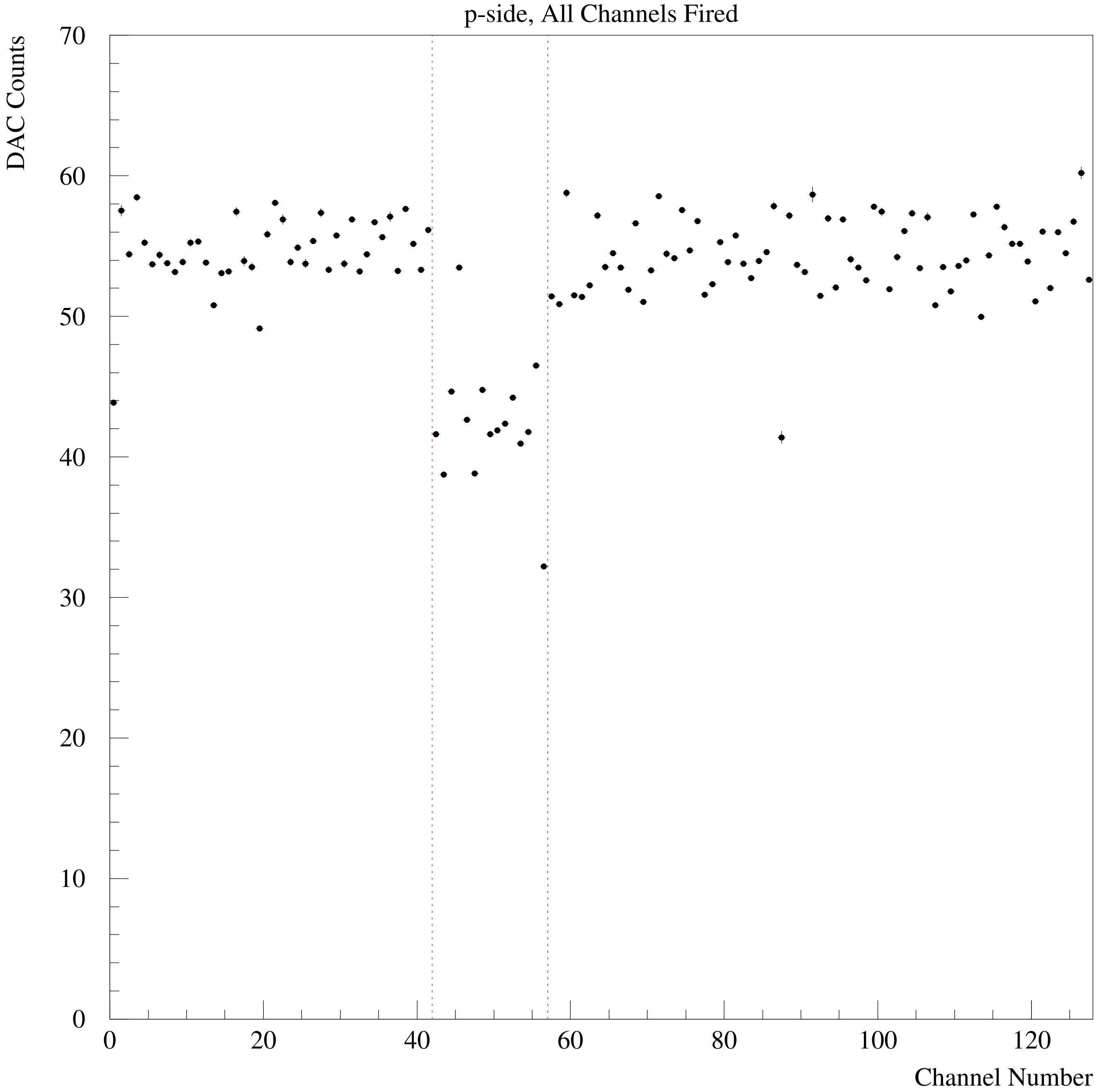


Chip 4 Channel 29

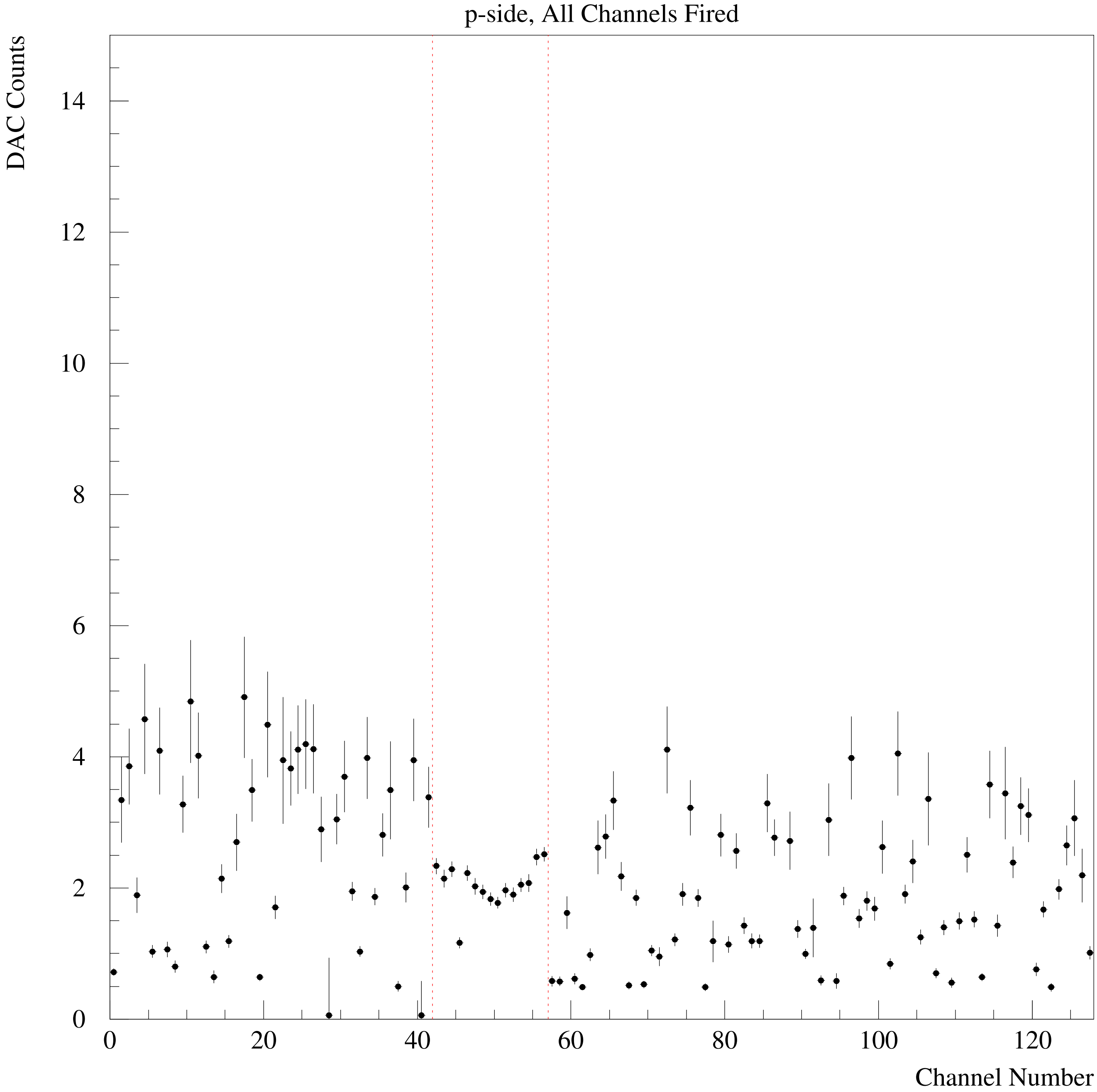


Chip 4 Channel 46

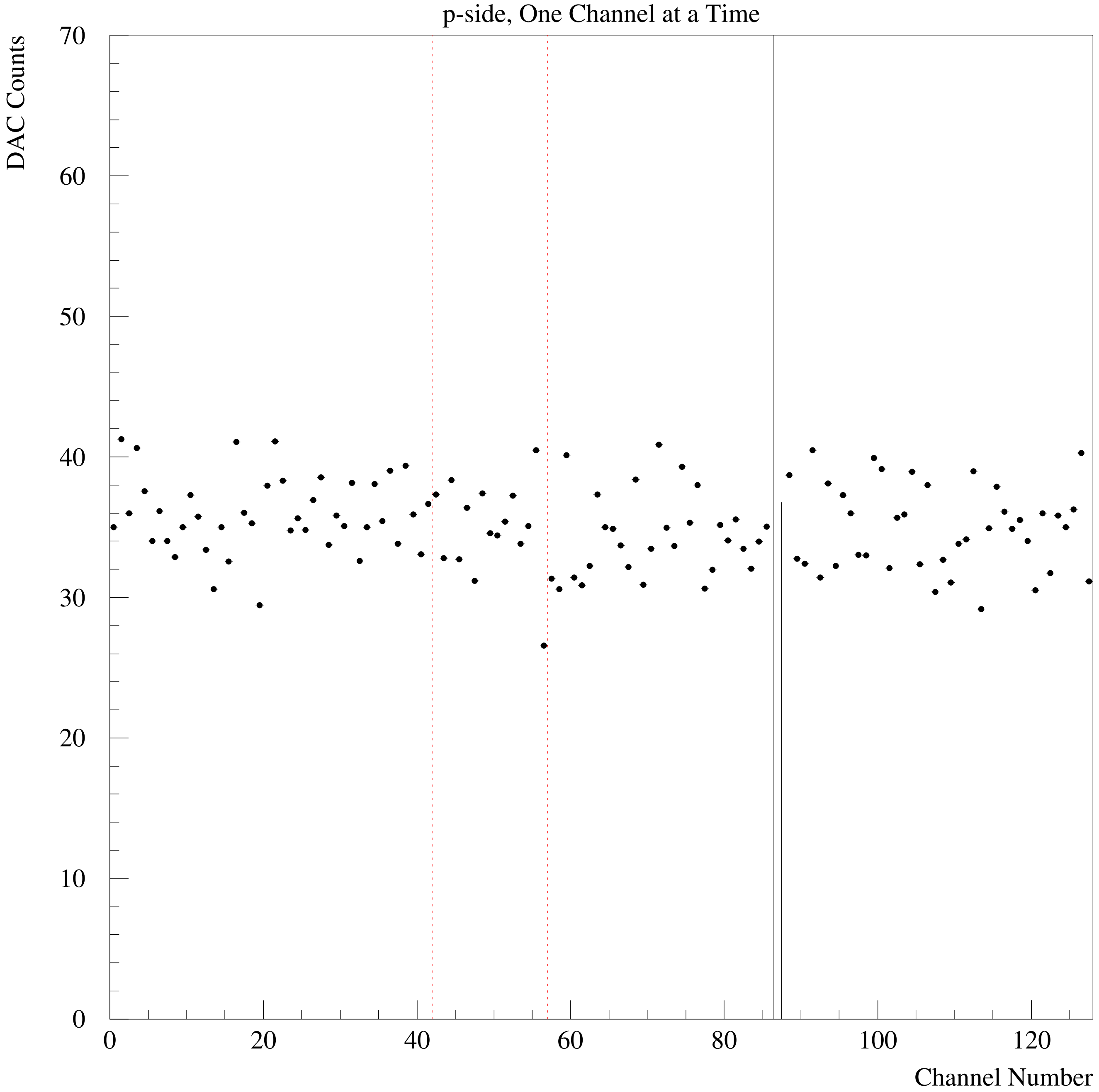
97/03/26 21.48



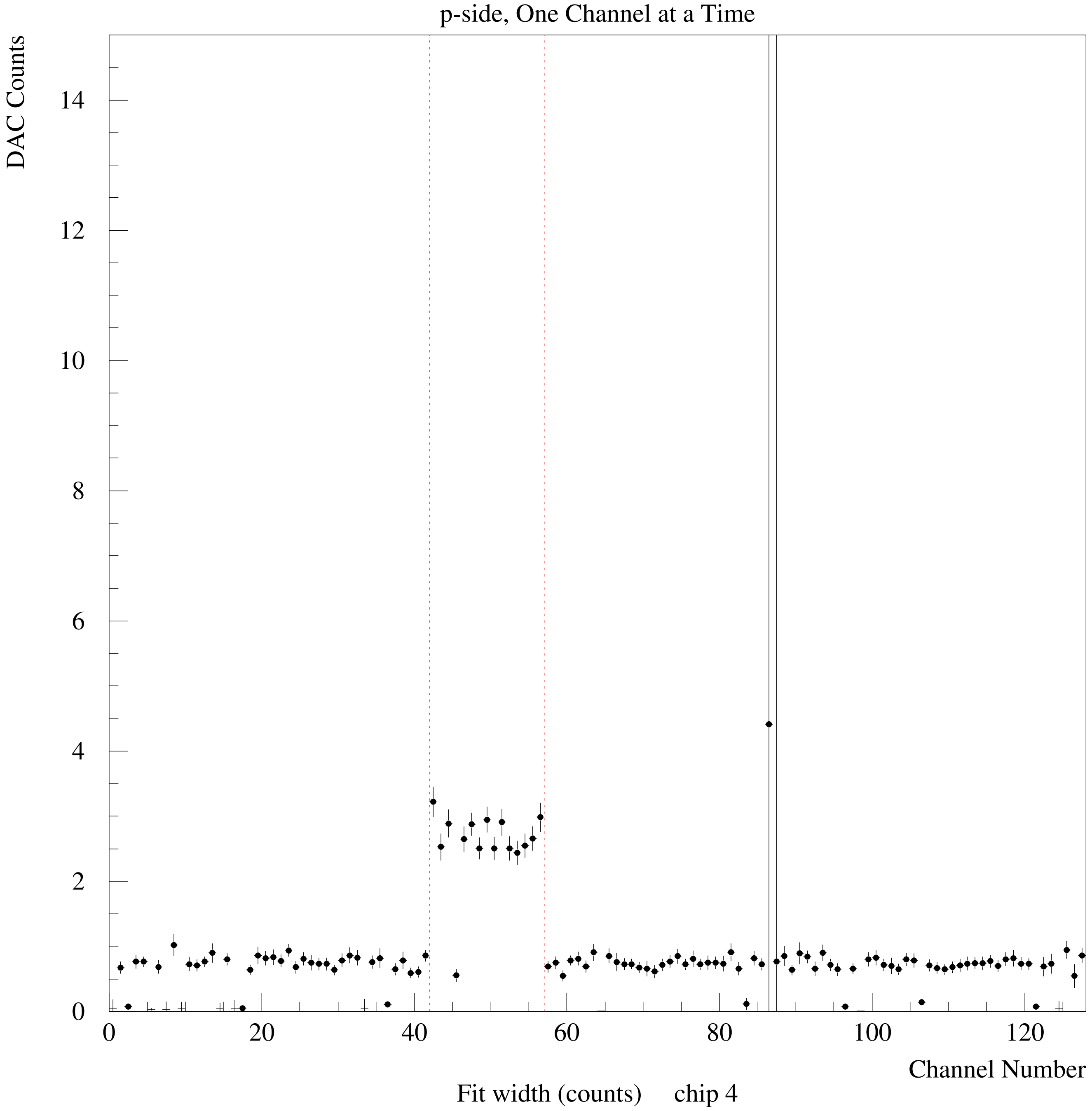
Fit threshold (counts) chip 4



Fit width (counts) chip 4



Fit threshold (counts) chip 4


p-side, One Channel at a Time
DAC Counts
14
12
10
8
6
4
2
0
0
20
40
60
80
100
120
Channel Number
Fit width (counts) chip 4